

EE4325 - Introduction to VLSI Design

[EE 4325](#) Introduction to VLSI Design (3 semester credit hours) Introduction to CMOS digital IC design using semi-custom and full-custom design techniques with an emphasis on techniques for rapid prototyping and use of various VLSI design tools. FPGA's, standard cell and full-custom design styles. Introduction to a wide variety of CAD tools. Prerequisite: [CE 3320](#) or [EE 3320](#) (or, for CS majors, [CS 4341](#)). (3-0) T