Electrical Engineering: Digital Systems

**EEDG 5325 (CE 5325)** Hardware Modeling Using HDL (3 semester credit hours) This course introduces students to hardware description languages (HDL) beginning with simple examples and describing tools and methodologies. It covers the language, dwelling on fundamental simulation concepts. Students are also exposed to the subset of HDL that may be used for synthesis of custom logic. HDL simulation and synthesis labs and projects are performed using commercial and/or academic VLSI CAD tools. Prerequisite: EE 3320 or equivalent. (3-0) T

**EEDG 6301 (CE 6301)** Advanced Digital Logic (3 semester credit hours) Modern design techniques for digital logic. Logic synthesis and design methodology. Link between front-end and back-end design flows. Field programmable gate arrays and reconfigurable digital systems. Introduction to testing, simulation, fault diagnosis and design for testability. Prerequisites: EE 3320 or equivalent and background in VHDL/Verilog. (3-0) T

**EEDG 6302 (CE 6302)** Microprocessor and Embedded Systems (3 semester credit hours) Design of microprocessor based systems including I/O and interface devices. Programming of micro-controllers and embedded systems. Microprocessor architectures. Use of emulators and other sophisticated test equipment. Extensive laboratory work. Lab fee of $30 required. Prerequisites: EE 4304 or equivalent and background in VHDL/Verilog. (2-3) Y

**EEDG 6303 (CE 6303)** Testing and Testable Design (3 semester credit hours) Techniques for detection of failures in digital circuits and systems. Fault modeling and detection. Functional testing and algorithms for automatic test pattern generation (ATPG). Design of easily testable digital systems. Techniques for introducing built-in self test (BIST) capability. Test of various digital modules, such as PLA's, memory circuits, datapath, etc. Prerequisites: EE 3320 or equivalent and background in VHDL/Verilog. (3-0) Y

**EEDG 6304 (CE 6304) and CS 6304** Computer Architecture (3 semester credit hours) Trends in processor, memory, I/O and system design. Techniques for quantitative analysis and evaluation of computer systems to understand and compare alternative design choices in system design. Components in high performance processors and computers: pipelining, instruction level parallelism, memory hierarchies, and input/output. Students will undertake a major computing system analysis and design project. Must have an understanding of C/C++. Prerequisite: CS 2340 or EE 4304. (3-0) Y

**EEDG 6306 (CE 6306)** Application Specific Integrated Circuits Design (3 semester credit hours) This course discusses the design of application specific integrated circuits (ASIC). Specific topics include: VLSI system design specification, ASIC circuit structures, synthesis, and implementation of an ASIC digital signal processing (DSP) chip. Prerequisite: EE 3320. (3-0) Y

**EEDG 6308 (CE 6308) and CS 6396** Real-Time Systems (3 semester credit hours) Introduction to real-time applications and concepts. Real-time operating systems and resource management. Specification and design methods for real-time systems. System performance analysis and optimization techniques. Project to specify, analyze, design, implement and test small real-time system. Prerequisite: CS 5348. (3-0) R

**EEDG 6309 (CE 6309)** Applications of Machine Learning in Semiconductor IC Manufacturing and Test (3 semester credit hours) Fundamentals of machine learning, including regression, classification, feature extraction, feature selection, synthetic training set enhancement, boosting and curse of dimensionality; test
cost reduction via test compaction, alternate test, adaptive test and effectiveness metrics; wafer-level spatial and spatio-temporal correlation modeling, process variation decomposition, process monitoring, outlier detection yield prediction; post-manufacturing tuning and post-deployment calibration of analog/RF ICs; security and trust assessment, including hardware Trojan detection, counterfeit IC identification, and fab-of-origin attestation. Experience with Machine Learning methods and software desirable but not required. Prerequisite: CE 6301 or EEDG 6301 or CE 6303 or EEDG 6303 or CE 6325 or EECT 6325 or EECT 6326. (3-0) Y

**EEDG 6310 (CE 6310)** Hardware Security (3 semester credit hours) Theory of cryptography for security, an overview of both classical and emerging attack methods and methodologies and possible defenses against them with respect to silicon-on-chip security, side-channel attacks, hardware trojans, physically unclonable functions, IC counterfeit protection, and hardware-based malware detection. (3-0) Y

**EEDG 6312 (CE 6312)** Computing with Emerging Technologies (3 semester credit hours) Integration of emerging technologies in novel computing systems. Relevant devices include various spintronic devices, carbon nanotubes, graphene, memristors, and multi-gate FETs. Relevant computational functions include Boolean logic, as well as neuromorphic, threshold, stateful, quantum, and stochastic computing systems. (3-0) Y

**EEDG 6330 (CE 6330)** Applied Cryptography (3 semester credit hours) This course presents a wide range of cryptographic principles and their implementation in software/hardware. This includes: security properties; block and stream ciphers, their basic implementations, and various ways to attack them including side-channel attacks; public-key schemes using number/group-theoretic techniques; advanced protocols; and resilient implementation techniques. Basic familiarity with algorithms, probability, and algebra needed. Credit cannot be received for both CS 6377 and (CE 6330 or EEDG 6330). Prerequisite: ENCS majors only. (3-0) Y

**EEDG 6331 (CE 6331)** High-Level Synthesis: Design and Verification (3 semester credit hours) Facilitate the design of dedicated hardware using higher levels of abstraction (ANSI-C, C++ or SystemC) instead of hardware description languages like Verilog or VHDL. Theory of HLS process is comprehensively studied including: technology independent optimizations, resource allocation, scheduling, and binding stages. Students will design different types of hardware accelerators using HLS and learn how to design and verify complete hardware systems using only C. Course projects may include, but are not limited to: Building an automated HLS design space explorer, design of neural networks and building complete systems in C. Commercially available EDA tools will be used during the course. Prerequisite: EE 3320 or equivalent, C/C++. (3-0) Y

**EEDG 6370 (CE 6370)** Design and Analysis of Reconfigurable Systems (3 semester credit hours) Introduction to reconfigurable computing, programmable logic: FPGAS, CPLDs, CAD issues with FPGA based design, reconfigurable systems: emulation, custom computing, and embedded application based computing, static and dynamic hardware, evolutionary design, software environments for reconfigurable systems. Prerequisite: EE 3320 or equivalent. (3-0) R

**EEDG 6375 (CE 6375)** Design Automation of VLSI Systems (3 semester credit hours) This course deals with various topics related to the development of CAD tools for VLSI systems design. Algorithms, data structures, heuristics and design methodologies behind CAD tools. Design and analysis of algorithms for layout, circuit partitioning, placement, routing, chip floor planning, and design rule checking (DRC). Introduction to CAD algorithms for RTL and behavior level synthesis, module generators, and silicon compilation. (3-0) Y

**EEDG 7V81** Special Topics in Digital Systems (1-6 semester credit hours) May be repeated for credit as topics
vary (9 semester credit hours maximum). ([1-6]-0) R