**TE4341 - Digital Logic and Computer Design**

*TE 4341* Digital Logic and Computer Design (3 semester credit hours) Boolean algebra and logic circuits; synchronous sequential circuits; gate level design of ALU, registers, and memory unit; register transfer operations; design of data path and control unit for a small computer; Input-Output interface. Credit cannot be received for both courses, ([CS 4341](https://catalog.utdallas.edu/2018/undergraduate/courses/cs4341) or [TE 4341](https://catalog.utdallas.edu/2018/undergraduate/courses/te4341)) and ([CE 3320](https://catalog.utdallas.edu/2018/undergraduate/courses/ce3320) or [EE 3320](https://catalog.utdallas.edu/2018/undergraduate/courses/ee3320)). Prerequisites: ([CE 2310](https://catalog.utdallas.edu/2018/undergraduate/courses/ce2310) or [EE 2310](https://catalog.utdallas.edu/2018/undergraduate/courses/ee2310)) or ([CS 3340](https://catalog.utdallas.edu/2018/undergraduate/courses/cs3340) or [SE 3340](https://catalog.utdallas.edu/2018/undergraduate/courses/se3340) or [TE 3340](https://catalog.utdallas.edu/2018/undergraduate/courses/te3340)) and [PHYS 2326](https://catalog.utdallas.edu/2018/undergraduate/courses/phys2326). Corequisite: ([CS 4141](https://catalog.utdallas.edu/2018/undergraduate/courses/cs4141) or [TE 4141](https://catalog.utdallas.edu/2018/undergraduate/courses/te4141)). (Same as [CS 4341](https://catalog.utdallas.edu/2018/undergraduate/courses/cs4341)) (3-0) S