

CE7328 - Physical Design of High-Speed VLSI Circuits

[CE 7328](#) ([EEDG 7328](#)) Physical Design of High-Speed VLSI Circuits (3 semester credit hours)

Techniques for the physical design of high-speed VLSI circuits. Topics related to interconnection circuit modeling, performance-driven routing, buffer and wire sizing, placement and floor planning, technology mapping and performance evaluation issues encountered in high-speed VLSI circuit designs. Discussion of state-of-the-art practical industrial design examples. A project related to the development of a prototype CAD tool. Prerequisites: (CE or [EECT 6325](#)) and knowledge of programming in C. (3-0) T