EEDG6375 - Design Automation of VLSI Systems

EEDG 6375 (CE 6375) Design Automation of VLSI Systems (3 semester credit hours) This course deals with various topics related to the development of CAD tools for VLSI systems design. Algorithms, data structures, heuristics and design methodologies behind CAD tools. Design and analysis of algorithms for layout, circuit partitioning, placement, routing, chip floor planning, and design rule checking (DRC). Introduction to CAD algorithms for RTL and behavior level synthesis, module generators, and silicon compilation. Prerequisite: CS 5343. Corequisite: EECT 6325. (3-0) Y (2016-02-06 00:09:23)