EEDG6301 - Advanced Digital Logic

EEDG 6301 (CE 6301) Advanced Digital Logic (3 semester credit hours) Modern design techniques for digital logic. Logic synthesis and design methodology. Link between front-end and back-end design flows. Field programmable gate arrays and reconfigurable digital systems. Introduction to testing, simulation, fault diagnosis and design for testability. Prerequisites: EE 3320 or equivalent and background in VHDL/Verilog. (3-0) T (2016-02-06 00:09:30)