EESC 6367 - Applied Digital Signal Processing

EESC 6367  Applied Digital Signal Processing (3 semester hours) Implementation of signal processing algorithms, combination of textual and graphical programming of DSP systems, fixed-point versus floating-point, FPGA/DSP chip architecture, FPGA/DSP software development tools, code optimization, application project. Prerequisites: EE 4361 or equivalent and knowledge of C or MATLAB. (2-3) Y (2016-02-05 21:56:29)