EEDG6398 - DSP Architectures

EEDG 6398 (CE 6398, CS 6398) DSP Architectures (3 semester hours) Typical DSP algorithms, representation of DSP algorithms, data-graph, FIR filters, convolutions, Fast Fourier Transform, Discrete Cosine Transform, low power design, VLSI implementation of DSP algorithms, implementation of DSP algorithms on DSP processors, DSP applications including wireless communication and multimedia. Prerequisite: CS 5343. (3-0) Y (2016-02-05 21:13:50)